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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10041791	FILING DATE 10/19/2001	CLASS 438	SUBCLASS	GAU 2812	EXAMINER
<b>**APPLICANTS:</b> Hirano Tomoyuki, Asada Kazumi					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN P2000-319402 10/19/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's initials				ATTORNEY DOCKET NO 09792909 5225	
TITLE Method of manufacturing semiconductor device					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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